

FIG.1

20 DUPLEXER
 30 RECEIVER
 40 HS-PDSCH RECEIVE PRODCESSOR
 5 401 DESPREADER
 402 DEMODULATOR
 403 DECODER
 404 ERROR DETECTOR
 50 HS-SCCH RECEIVE PROCESSOR
 10 501 DESPREADER
 502 DEMODULATOR
 503 DECODER
 504 DETERMINER
 60 CPICH DESPREADER
 15 61 SIR MEASURER
 62 CQI SELECTOR
 70 DPCH DESPREADER
 71 SIGNALING DETECTOR
 72 CONTROLLER
 20 80 TRANSMITTER
 パケットデータ PACKET DATA

FIG.2

START HSDPA
 25 ST10 HS-SCCH MONITORING LOOP
 ST31 DECODE HS-SCCH
 ST32 IS HS-SCCH FOR THIS MOBILE STATION?

ST33 DECODE HS-PDSCH
 ST40 TRANSMIT ACK/NACK SIGNAL
 ST50 DETECT GAP TIMING
 ST60 DOES GAP TIMING OVERLAP ACK/NACK SIGNAL TRANSMISSION
 5 TIMING?
 ST70 STOP RECEIVE PROCESSING
 ST20 HS-SCCH MONITORING LOOP
 HSDPA END

10 FIG. 3
 1 SLOT
 UPLINK DPCH
 SUB-FRAME (3 SLOTS)
 COMPRESSED MODE GAP
 15 TRANSMISSION START TIMING
 TRANSMISSION END TIMING
 RECEPTION START TIMING
 RECEPTION END TIMING
 7.5 SLOTS
 20 STOP RECEIVE PROCESSING

FIG. 4
 START HSDPA
 ST10 HS-SCCH MONITORING LOOP
 25 ST31 DECODE HS-SCCH
 ST32 IS HS-SCCH FOR THIS MOBILE STATION?
 ST33 DECODE HS-PDSCH

ST41 TRANSMIT ACK /NACK SIGNAL

ST51 DETECT THE NUMBER OF RETRANSMISIONS

ST71 STOP RECEIVE PROCESSING DEPENDING ON THE NUMBER OF
RETRNASMISSIONS

5 ST20 HS-SCCH MONITORING LOOP
HSDPA END

FIG. 5

図 3 参照

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FIG. 6

START HSDPA

ST10 HS-SCCH MONITORING LOOP

ST31 DECODE HS-SCCH

15 ST32 IS HS-SCCH FOR THIS MOBILE STATION?

ST33 DECODE HS-PDSCH

ST40 TRANSMIT ACK /NACK SIGNAL

ST52 DETECT CHANGE TIMING

ST62 IS TRASMISSION POSSIBLE BEFORE CHANGE TIMING?

20 ST71 STOP RECEIVE PROCESSING AND TRANSNMIT PROCESSING

ST20 HS-SCCH MONITORING LOOP

HSDPA END

FIG. 7

25 HSPDA SERVING CELL CHANGE TIMING

図 3 参照

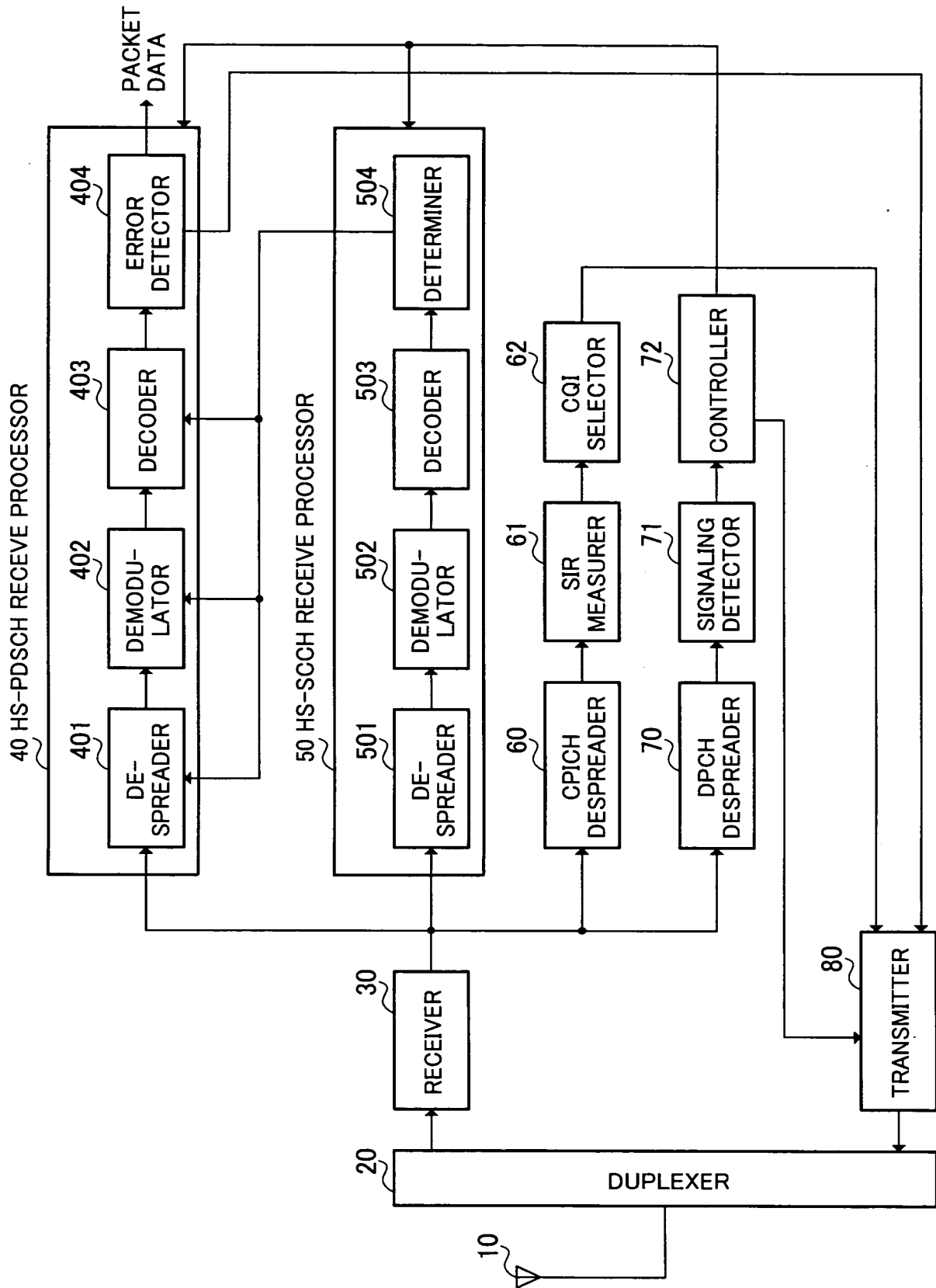


FIG.1

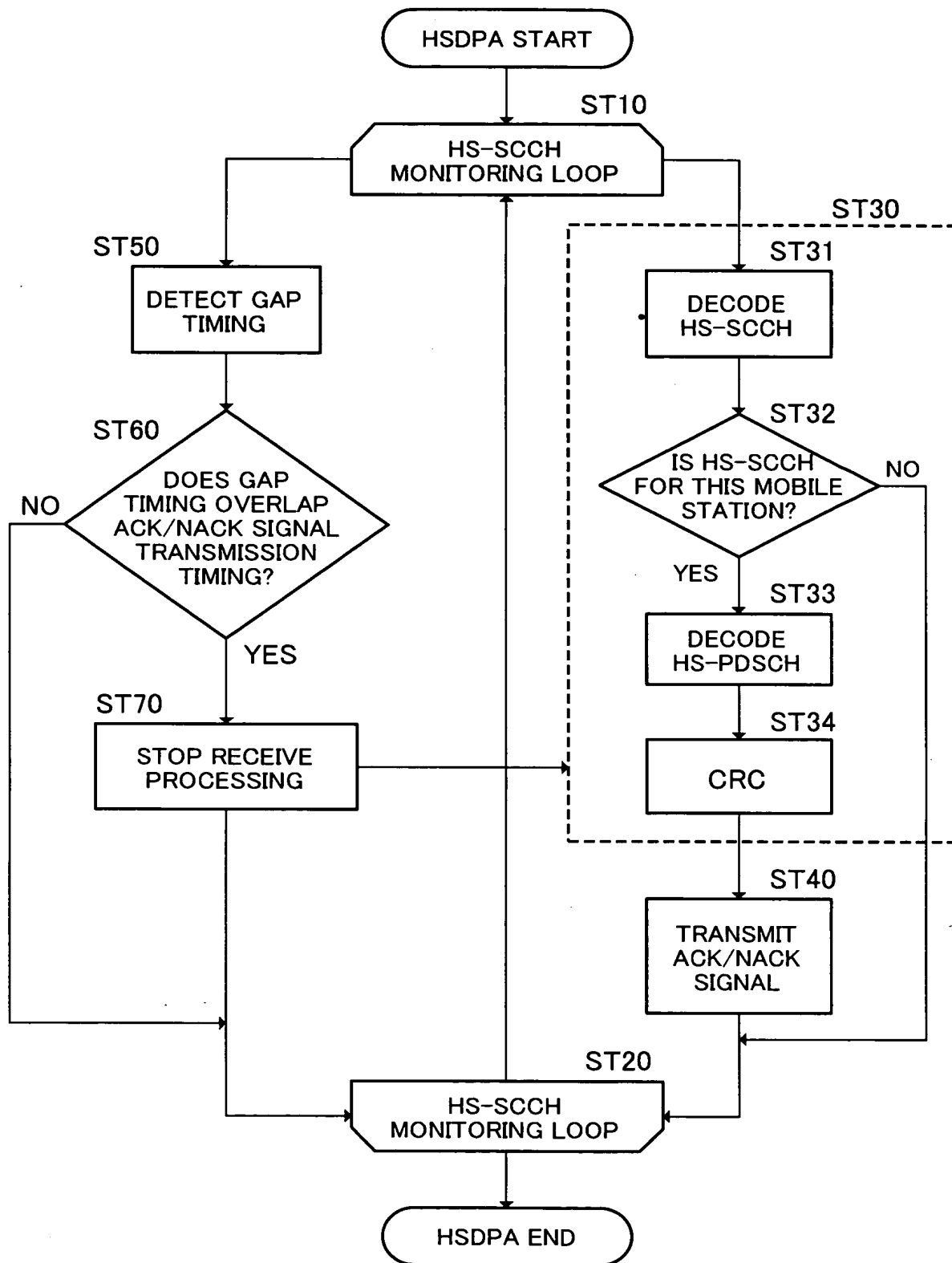


FIG.2

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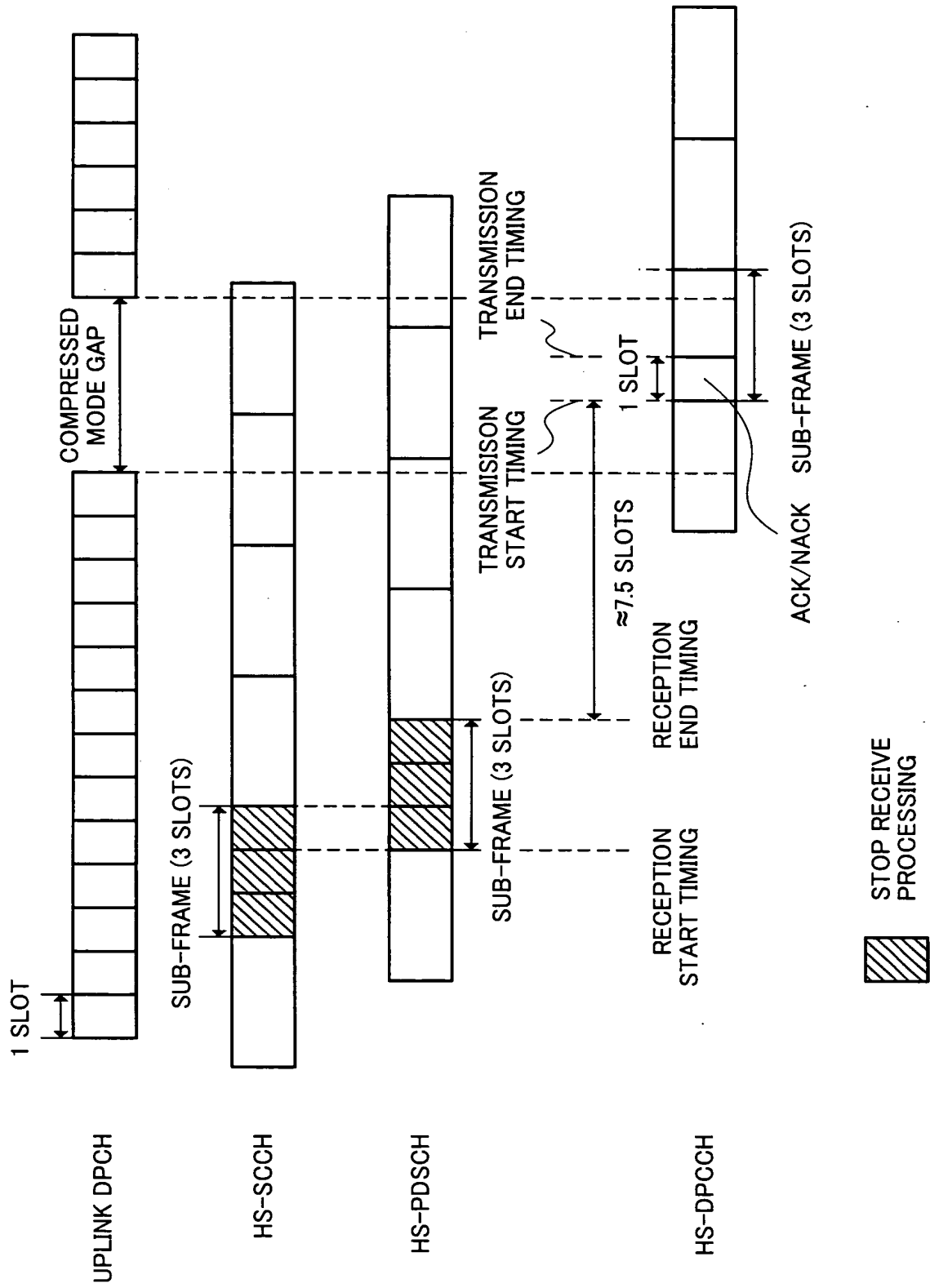


FIG.3

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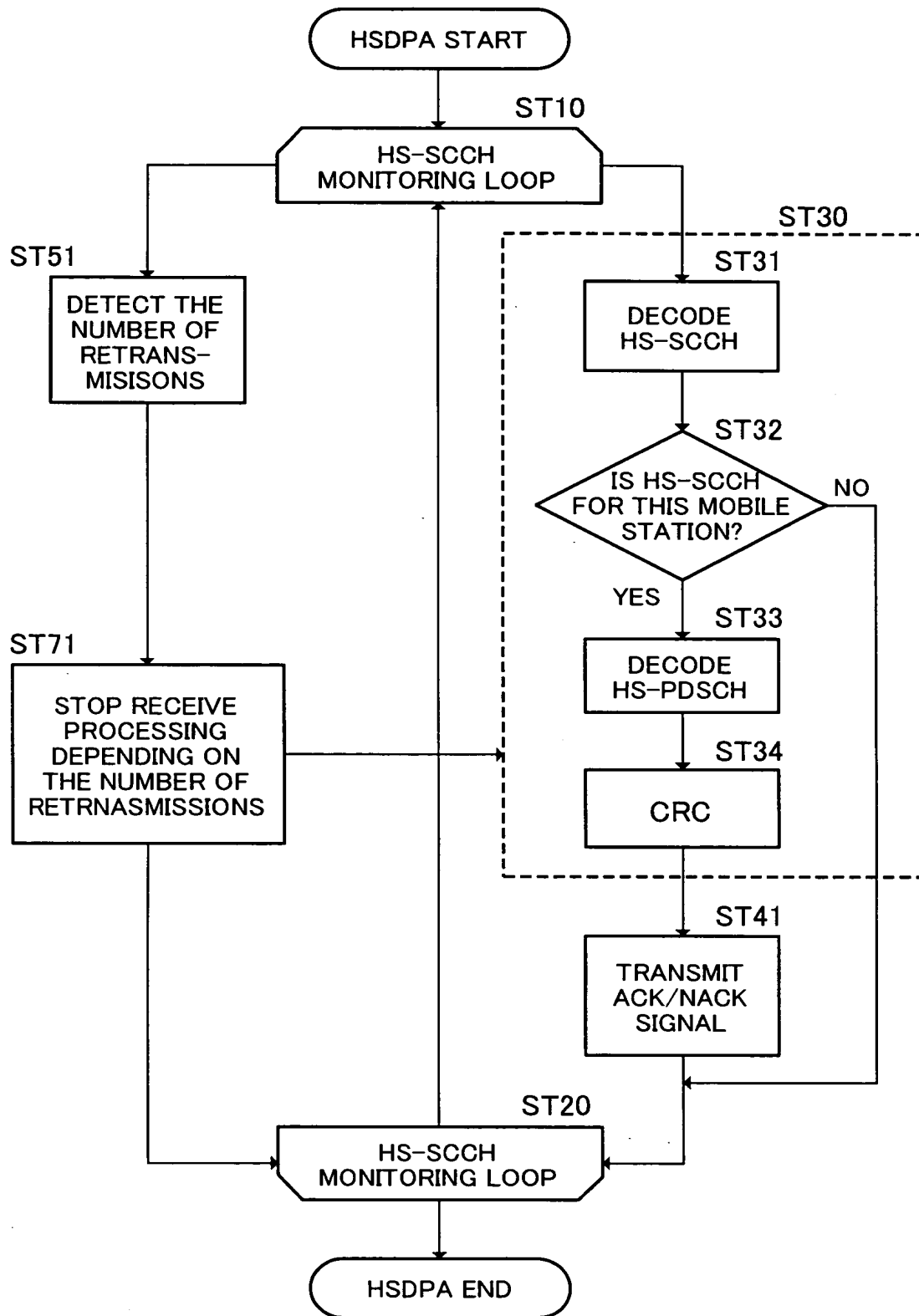


FIG.4

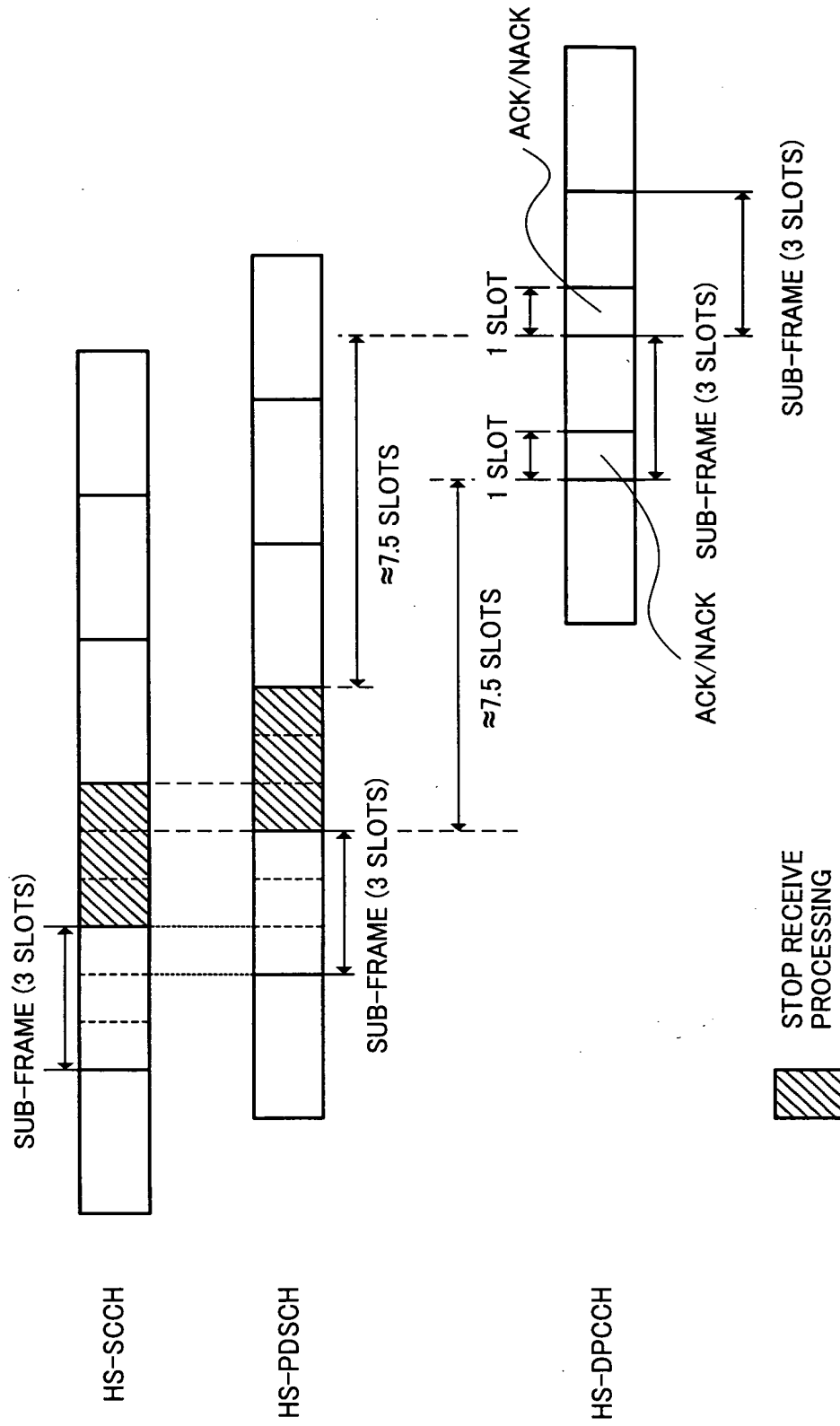


FIG.5

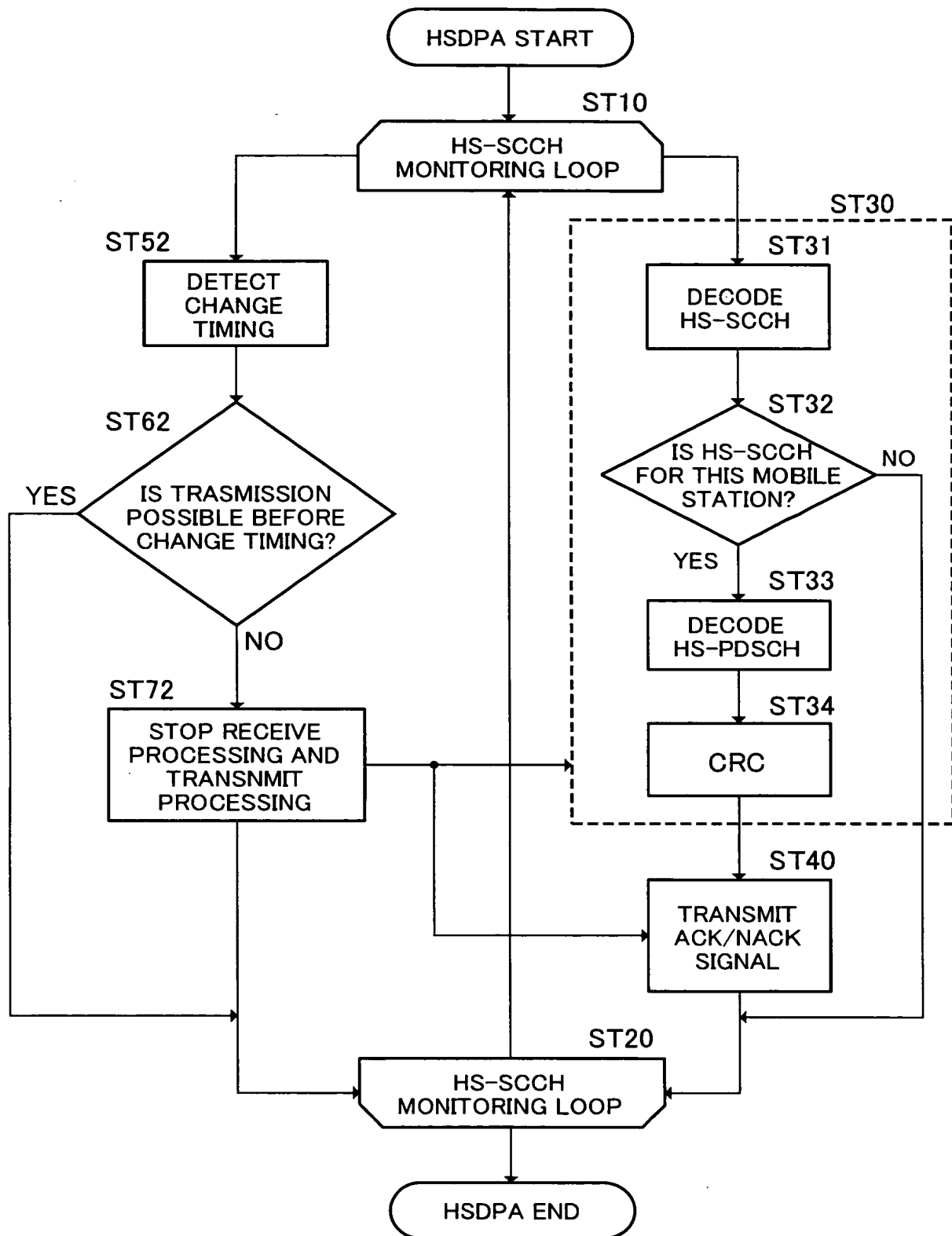


FIG.6

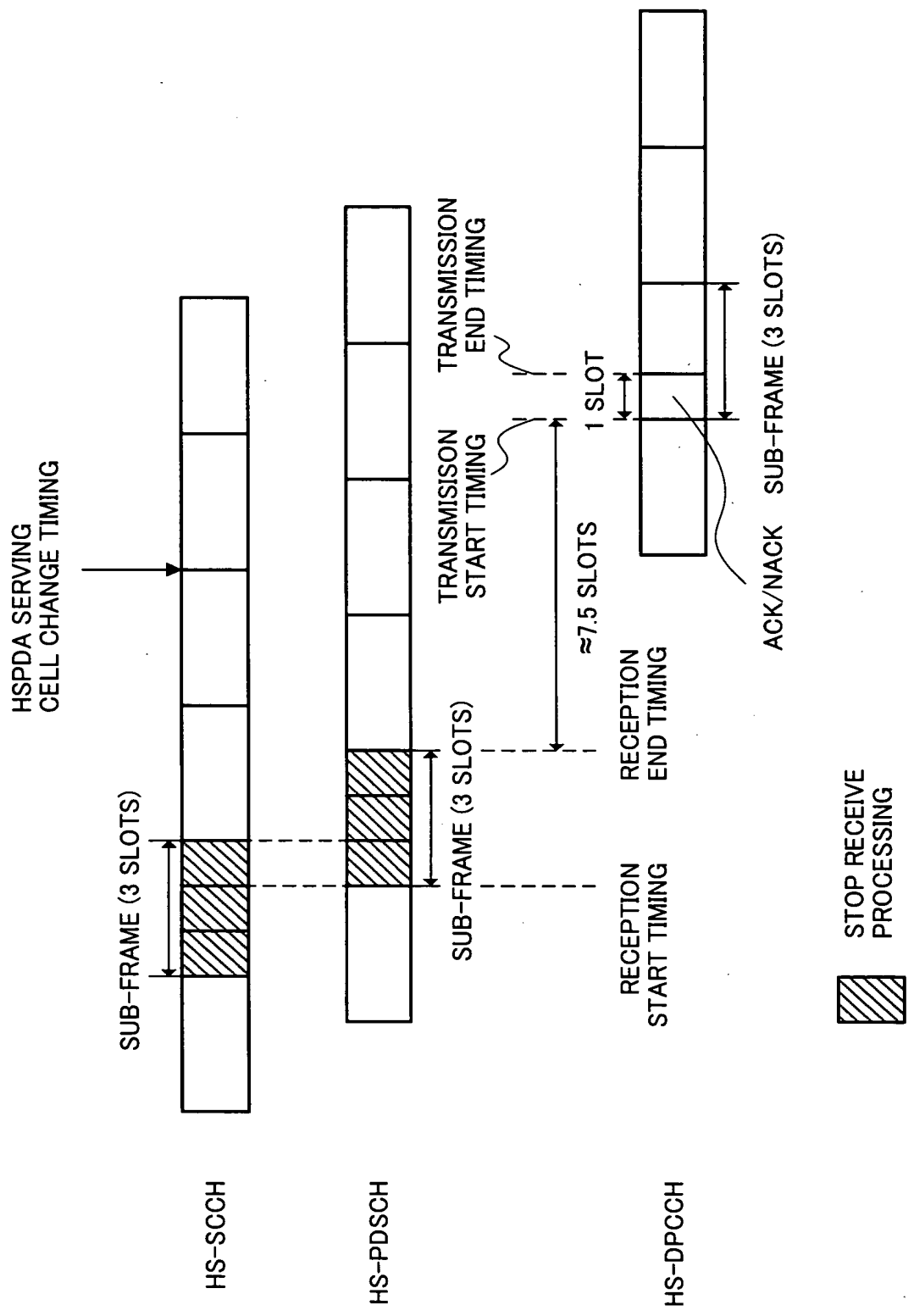


FIG.7